

MULTIPROCESSOR SYSTEM

11

Patent number: JP60173654
Publication date: 1985-09-07
Inventor: UEDA KENICHI; SUGANO ATSUSHI; HONDA KUNIO;
OKAMURA YOSHIKI
Applicant: MATSUSHITA ELECTRIC IND CO LTD
Classification:
- **international:** G06F11/00; G06F15/16
- **european:** G06F11/20; G06F11/22
Application number: JP19840028944 19840217
Priority number(s): JP19840028944 19840217

Abstract of JP60173654

PURPOSE: To allow a faulty processor to communicate for diagnostic/test processing by providing a table on which two kinds of logical processor numbers are given to each processor executing data processing even if the data processing executed by the faulty processor is performed by a substituted processor. **CONSTITUTION:** A table 12 storing logical processor number and a physical processor number corresponding to processors 11-1n is provided in a system control processor 2. Then the logical processor number of each processor at diagnosis/test and data processing and the physical processor number corresponding to them are given to the table 12. If a fault takes place in a processor 11, a spare processor 1n+1 as a spare executes data processing as a substituted processor and the system control processor 2 conducts diagnosis and test to the processor 11.

